



Course Tittle: DC Machines Lab

Following documents are available in Course File.

S.No.	Points	Yes	No
1	Institute and Department Vision and Mission Statements	J	
2	PEO & PO Mapping	J	
3	Academic Calendar	J	
4	Subject Allocation Sheet	J	
5	Class Time Table, Individual Timetable (Single Sheet)	J	
6	Syllabus Copy	J	
7	Course Handout	J	
8	CO-PO Mapping	1	
9	CO-Cognitive Level Mapping	1	
10	Lecture Notes		J
11	Tutorial Sheets With Solution		J
12	Soft Copy of Notes/Ppt/Slides		J
13	Sessional Question Paper and Scheme of Evaluation		J
14	Best, Average and Weak Answer Scripts for Each Sessional Exam. (Photocopies)		J
15	Assignment Questions and Solutions	1	
16	Previous University Question Papers		J
17	Result Analysis	J	
18	Feedback From Students	J	
19	Course Exit Survey		J
20	CO Attainment for All Mids.		J
21	Remedial Action.		J

Course Instructor / Course Coordinator
(Name)

Course Instructor / Course Coordinator (Signature)





Department/Program-EEE

MISSION OF THE INSTITUTE:

To achieve and impart quality education with an emphasis on practical skills and social relevance.

VISION OF THE INSTITUTE:

To be among the best of the institutions for engineers and technologists with attitudes, skills and knowledge and to become an epicenter of creative solutions.

VISION OF THE PROGRAM:

To impart technical knowledge and skills required to succeed in life, career and help society to achieve self-sufficiency.

MISSION OF THE PROGRAM:

- To become an internationally leading department for higher learning.
- To build upon the culture and values of universal science and contemporary education.
- To be a center of research and education generating knowledge and technologies which lay groundwork in shaping the future in the fields of electrical and electronics engineering.
- To develop partnership with industrial, R&D and government agencies and actively participate in conferences, technical and community activities.





Program Educational Objectives (PEO's):

- **PEO 1:** Graduates will have a successful technical or professional careers, including supportiveand leadership roles on multidisciplinary teams.
- **PEO 2:** Graduates will be able to acquire, use and develop skills as required for effective professional practices.
- **PEO 3:** Graduates will be able to attain holistic education that is an essential prerequisite forbeing a responsible member of society.
- **PEO 4:** Graduates will be engaged in life-long learning, to remain abreast in their profession andbe leaders in our technologically vibrant society.

Program outcomes

- a) Ability to apply knowledge of mathematics, science, and engineering.
- b) Ability to design and conduct experiments, as well as to analyze and interpret data.
- c) Ability to design a system, component, or process to meet desired needs within realistic constraints such as economic, environmental, social, political, ethical, health and safety, manufacturability, and sustainability.
- d) Ability to function on multi-disciplinary teams.
- e) Ability to identify, formulates, and solves engineering problems.
- f) Understanding of professional and ethical responsibility.
- g) Ability to communicate effectively.
- h) Broad education necessary to understand the impact of engineering solutions in a global, economic, environmental, and societal context.
- i) Recognition of the need for, and an ability to engage in life-long learning.
- j) Knowledge of contemporary issues.
- k) Ability to utilize experimental, statistical and computational methods and tools necessary for engineering practice.
- Graduates will demonstrate an ability to design electrical and electronic circuits, power electronics, power systems; electrical machines analyze and interpret data and also an ability to design digital and analog systems and programming them.

PEOs & POs Mapping

Programme Educational	Programme Outcomes (POs)											
Objectives (PEOs)	1	2	3	4	5	6	7	8	9	10	11	12
1	M	M	-	-	Н	1	-	Н	Н	ı	Н	Н
2	-	-	M	M	Н	Н	Н	-	-	•	•	Н
3	-	-	-	-	Н	Н	M	M	M	M	Н	Н
4	-	-	-	M	M	Н	M	Н	Н	-	M	Н

^{*} H: Strongly Correlating (3); M: Moderately Correlating (2)& L: Weakly Correlating (1)



Department of Electrical & Electronics Engineering COURSE OBJECTIVES

Academic	Vear	: 2018-2019
1 KCUUCIIIIC	1 Cai	. 2010 2017

Semester : I

Name of the Program: EEE...... B.Tech ...II/I..... Section: A,B

Course/Subject: DC Machines Lab.....Code:GR17A2037

Name of the Faculty: D.Srinivasa Rao Dept:EEE.....

Designation: Assistant professor

On completion of this Subject/Course the student shall be able to:

S.No	Course Objectives						
1.	Strong background in different types of excitation for dc motors and generators						
2.	Mathematical foundation and there by the relative production of emf with respect to flux.						
3.	Knowledge on various lab experiments connected with dc motors.						
4.	Knowledge on various lab experiments connected with dc generators and there by achieve the						
	design concepts.						
5.	Knowledge on application of dc motor concepts with respect to the performance						
	characteristics of dc motors.						
6.	Knowledge on application of dc generator concepts with respect to the performance						
	characteristics of dc generators.						
7.	Basic knowledge of drive systems for further study at post graduate level.						



Department of Electrical & Electronics Engineering COURSE OUTCOMES

Academic Year		: 2018-2019)						
Semester		: I							
Name of the	Program:	EEE	B.Tech	II/I	Section: A,B				
Course/Subject: DC Machines Lab Code:GR17A2037									
Name of the Faci	ulty: D.Sriniv		Dept:	EEE					
Designation: Assistant professor									

The expected outcomes of the Course/Subject are:

S.No	Course Outcomes
1.	Have knowledge of various parts of a electrical DC machines
2.	Develop knowledge helpful for application of dc machines.
3.	Conduct speed control of different types of DC Motors.
4.	Use characteristics of various generators depending on their type of field excitation.
5.	Understand the concept of different types of windings viz lap and wave for armature
6.	Perform test on Motor-Generator Set.
7.	Know the concept of commutation dc machines for conversion of AC to DC or DC to AC.

Signature of HOD	Signature of faculty
Signature of HOD	Signature of faculty

Date:



Department of Electrical & Electronics Engineering

ACADEMIC CALENDAR Academic Year 2018-19

<u>II B.TECH – FIRST SEMESTER</u>

S. No.	EVENT	PERIOD	DURATION
1	1 st Spell of Instructions	02-07-2018 to 05-09-2018	9 Weeks 3 Days
2	1 st Mid-term Examinations	06-09-2018 to 08-09-2018	3 Days
3	2 nd Spell of Instructions	10-09-2018 to 27-10-2018	7 Weeks
4	2 nd Mid-term Examinations	29-10-2018 to 31-10-2018	3 Days
5	Preparation	01-11-2018 to 07-11-2018	1 Week
6	End Semester Examinations (Theory/	08-11-2018 to 08-12-2018	4 Weeks 3 Days
	Practicals) Regular/Supplementary		
7	Commencement of Second Semester,	10-12-2018	
	A.Y 2018-19		

II B.TECH – SECOND SEMESTER

S. No.	EVENT	PERIOD	DURATION
1	1 st Spell of Instruction	10-12-2018 to 06-02-2019	8 Weeks 3 days
2	1 st Mid-term Examinations	07-02-2019 to 09-02-2019	3 Days
3	2 nd Spell of Instruction	11-02-2019 to 03-04-2019	7 Weeks 3 Days
4	2 nd Mid-term Examinations	04-04-2019 to 06-04-2019	3 Days
5	Preparation	08-04-2019 to 17-04-2019	1 Week 3 Days
6	End Semester Examinations (Theory/	18-04-2019 to 08-05-2019	3 Weeks
	Practicals) Regular		
7	Supplementary and Summer Vacation	09-05-2019 to 22-06-2019	6 Weeks 3 Days
8	Commencement of First Semester,	24-06-2019	
	A.Y 2019-20		

Subject Code	Subject Name	Faculty Code	Faculty Name	Almai	nac
GR17A2058	Special Functions and Complex Variable	Dr GS	Dr G Swapna	1 st Spell of Instructions	02-07-2018 to 05- 09-2018
GR17A2076	Computer Organization	PRK	P Ravi Kanth	1 st Mid-term Examinations	06-09-2018 to 08- 09-2018
GR17A2034	Electromagnetic Fields	SN	Syed Sarfaraz Nawaz	2 nd Spell of Instructions	10-09-2018 to 27- 10-2018
GR17A2035	Network Theory	MS	M Srikanth	2 nd Mid-term Examinations	29-10-2018 to 31- 10-2018
GR17A2036	DC Machines and Transformers	Dr BPB	Dr B Phaneendra Babu	Preparation	01-11-2018 to 07- 11-2018
GR17A2037	DC Machines Lab	DSR/PRK	D Srinivasa Rao/P Ravikanth	End Semester Examinations	08-11-2018 to 08-
GR17A2038	Electrical Networks Lab	YSV / GBR	Y Satya Vani/ G Bhaskar Rao	(Theory/ Practicals) Regular / Supplementary	12-2018
GR17A2039	Electrical Simulation Lab	GSR/PS	G Sandhya Rani / P Sirisha	Commencement of Second Semester,	12/10/2018
GR17A2001	Environmental Science	Bh.SR	Bh. Saroja Rani	A.Y	12/10/2018





DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

GRIET/PRIN/06/G/01/18-19

wef: 02 July 2018

B Tech - FFF - A

GR17A2039

GR17A2001

Electrical Simulation Lab

Environmental Science

GSR/PS

 MHK

II Year - I Semester

B.Tech - EEE – A II Year - I Semester											
Day/Hour	9:00 - 9:45	9:45 - 10:3 0	10:30 - 11:15	11:15- 12:00	12:00 - 12:30	12:30 - 1:20	1:20 - 2:10	2:10 -3:00		Room No.	
MONDAY			b /DCM La A1/A2	b		ES	DCMT	DCMT	Theory	4401	
TUESDAY			ab / EN La A1/A2	b		DCMT	DCMT	NT	Lab	DCM Lab-2106 ES Lab- 4508	
WEDNESDA Y			ib / ES Lal A1/A2)	BREAK	NT	NT	СО	Lab	EN Lab- 4510	
THURSDAY	SFO	CV	E	MF	F AX		СО	СО	Class	V V S Madhuri	
FRIDAY	N ⁻	Т	E	MF		SFCV	СО	СО	Incharge :	V V 3 Mauriuri	
SATURDAY	EM	1F	D	CMT		SFCV	SFCV	NT			
Subject Code	Su	ıbject Na	ıme	Faculty Code		Faculty Nam	ne	Almanac			
GR17A2058		al Function		Dr GS	Dr G S	wapna		1 st Spell of I	nstructions	02-07-2018 to 05-09- 2018	
GR17A2076	Compu	ıter Orga	nization	PRK	P Ravi	Kanth		1 st Mid-term	Examinations	06-09-2018 to 08-09- 2018	
GR17A2034	Electro	magneti	c Fields	SN	Syed S	Syed Sarfaraz Nawaz		2 nd Spell of Instructions		10-09-2018 to 27-10- 2018	
GR17A2035	Net	twork The	eory	MS	M Srika	inth		2 nd Mid-term	Examinations	29-10-2018 to 31-10- 2018	
GR17A2036		Machines ansform		Dr BPB	Dr B Pl	naneendra Ba	abu	Preparation		01-11-2018 to 07-11- 2018	
GR17A2037	DC I	Machines	s Lab	DSR/MP	D Sriniv Prasha	rasa Rao/M End Semester nth Examinations (Theory/			08-11-2018 to 08-12-		
GR17A2038	Electric	al Netwo	orks Lab	YSV / GBR	Y Saty Rao	a Vani/ G Bh	askar	Practicals) F Supplement	Regular /	2018	
1	 			l				1		†	

G Sandhya Rani / P Sirisha

M Haritha Kiranmayi

Commencement of

Second Semester, A.Y

10-12-18







DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

B.Tech - EEE – B

Day/Hour	9:00 - 9:45	9:45 - 10:3 0	10:30 - 11:15	11:15- 12:00	12:00 - 12:30	12:30 - 1:20	1:20 - 2:10	2:10 -3:00			Room No.
MONDAY	EM	IF	D	CMT		SFCV	SFCV	ES		Theory	4402
TUESDAY	DCN	МТ	E	MF		NT	СО	CO		Lab	DCM Lab-2106 ES Lab- 4508
WEDNESDA Y	N ⁻	Γ	(co	BR.	SFCV	EMF	EMF		Lub	EN Lab- 4510
THURSDAY			b /DCM Lal B1/B2	b	BREAK	SFCV	SFCV	NT		Class	VVSMadhuri
FRIDAY			.ab / EN La B1/B2	b		NT	NT	ES		Incharge :	
SATURDAY			ib / ES Lat B1/B2)		СО	DCMT	DCMT			
Subject Code	Su	ıbject Na	me	Faculty Code		Faculty Nam			Almana	С	
GR17A2058		al Function		Dr GS	Dr G Sv	Swapna		1 st Spell of I	nstrı	uctions	02-07-2018 to 05-09- 2018
GR17A2076	Compu	iter Orga	nization	PRK	P Ravi	Kanth		1 st Mid-term	Exa	aminations	06-09-2018 to 08-09- 2018
GR17A2034	Electro	magneti	c Fields	SN	Syed S	arfaraz Nawa	rfaraz Nawaz 2 nd Spell of Instructions		ructions	10-09-2018 to 27-10- 2018	
GR17A2035	Net	work Th	eory	MS	M Srika	ınth		2 nd Mid-term	Exa	aminations	29-10-2018 to 31-10- 2018
GR17A2036	_	Machine: ansform		Dr BPB	Dr B Ph	naneendra Ba	abu	Preparation			01-11-2018 to 07-11- 2018
GR17A2037	DC I	Machine	s Lab	DSR/PR K	D Sriniv	D Srinivasa Rao/P Ravikanth		End Semest Examination		heory/	08-11-2018 to 08-12-
GR17A2038	Electric	al Netwo	orks Lab	YSV / GBR	Y Saty Rao	atya Vani/ G Bhaskar		Practicals) Regular / Supplementary		ılar / ¯	2018
GR17A2039	Electrica	al Simula	ation Lab	GSR/PS	G Sand	dhya Rani / P Sirisha Commencement of			10-12-18		
GR17A2001	Enviror	nmental	Science	Bh.SR	Bh. Sar	oja Rani		Second Semester, A.Y		10-12-18	



Department of Electrical & Electronics Engineering Individual timetable

Day/Hour	9:00 - 9:45	9:45 - 10:30	10:30 - 11:15	11:15- 12:00	12:00- 12:30	12:30 - 1:20	1:20 - 2:10	2:10 -3:00
MONDAY		DCN	/I LAB B2					
TUESDAY		DCN	/I LAB B1					
WEDNESDAY					BREAK			
THURSDAY		DCM	Л LAB A2		EAK			
FRIDAY		DCN	/I LAB A1					
SATURDAY								

	Room No.
Theory	4401
Lab	DCM Lab-2106 ES Lab- 4508 EN Lab- 4510
Class Incharge:	V V S Madhuri





Syllabus

Course Code: GR17A2037 L:0 T:0 P:2 C:2 63 GR17 Regulations

Contents

- 1. Speed Control of a D.C Shunt Motor
- 2. Brake Test on a DC Shunt Motor
- 3. Brake Test on a DC Compound Motor
- 4. Open Circuit Characteristics of a DC Shunt Generator
- 5. Load test on a D.C. Shunt Generator.
- 6. Load test on a D.C. Series Generator
- 7. Load test on D.C. Compound Generator
- 8. Hopkinson Test
- 9. Fields Test
- 10.Retardation Test on D.C. Shunt Motor
- 11.Swinburne's Test
- 12. Separation of Core Losses





COURSE SCHEDULE

Semester : I

Name of the Program: EEE...... B.Tech ...II/I..... Section: A,B

Course/Subject: DC Machines Lab..... Code:GR17A2037

Name of the Faculty: D.Srinivasa Rao Dept:EEE.....

Designation: Assistant professor

The Schedule for the whole Course / Subject is:

Ewa No	Description	Duration (Data)	Total No. of
Exp. No.	Description	Duration(Date)	Periods
1.	Speed Control of a D.C Shunt Motor	09/07/18	4
2.	Brake Test on a DC Shunt Motor	09/07/18	4
3.	Brake Test on a DC Compound Motor	16/07/18	4
4.	Open Circuit Characteristics of a DC Shunt Generator	16/07/18	4
5.	Load test on a D.C. Shunt Generator.	20/07/18	4
6.	Load test on a D.C. Series Generator.	23/07/18	4
7	Review of first cycle experiments	27/07/18	4
8.	Load test on a D.C. Compound Generator.	30/07/18	4
9.	Hopkinson Test	03/08/18	4
10	Fields Test	17/08/18	4
11.	Retardation Test on D.C. Shunt Motor	24/08/18	4
12.	Swinburne's Test	31/08/18	4
13.	Separation of Core Losses	14/09/18	4
14.	Internal Examination	21/09/18	4



Department of Electrical & Electronics Engineering

SCHEDULE OF INSTRUCTIONSCOURSEPLAN

Academic Year : 2018-2019

Semester : I

Name of the Program: EEE...... B.Tech ...II/I..... Section: A,B

Course/Subject: DC Machines Lab..... Code:GR17A2037

Name of the Faculty: D.Srinivasa Rao Dept:EEE.....

Designation: Assistant professor

		Objectives	References(TextBook,Journal)
Exp .No	Topics	& Outcome s	
1.	Speed Control of a D.C Shunt Motor	1,2,3 & 1,2	Electric Machines by I.J. Nagrath&D.P. Kothari
2.	Brake Test on a DC Shunt Motor	1,2,3 & 1,2	Electric Machines by I.J. Nagrath&D.P. Kothari
3	Brake Test on a DC Compound Motor	1,2,3& 1,2	Electric Machines by I.J. Nagrath&D.P. Kothari
4	Open Circuit Characteristics of a DC Shunt Generator	1	Electric Machines by I.J. Nagrath&D.P. Kothari
5	Load test on a D.C. Shunt Generator.	1,2,3& 1,2	Electric Machines by I.J. Nagrath&D.P. Kothari
6	Load test on a D.C. Series Generator.	1	Electric Machines by I.J. Nagrath&D.P. Kothari
7	Load test on a D.C. Compound Generator.		Electric Machines by I.J. Nagrath&D.P. Kothari
8	Hopkinson Test		Electric Machines by I.J. Nagrath&D.P. Kothari
9	Fields Test	1 ′ ′	Electric Machines by I.J. Nagrath&D.P. Kothari



Department of Electrical & Electronics Engineering

	Retardation Test on D.C. Shunt		Electric Machines by I.J. Nagrath&D.P.
10	Motor	1,2,3& 2	Kothari
11	Swinburne's Test	1,2,3,& 2	Electric Machines by I.J. Nagrath&D.P. Kothari
12	Separation of Core Losses	1,2,3,4 ,5,6 & 2	Electric Machines by I.J. Nagrath&D.P. Kothari

Assessment methods:

- 1. Operation skill and familiarization of software.
- 2. Experimental procedure, simulation results, internal observation, labrecord.
- 3. Internal examinations.
- 4. External examinations.
- 5. Viva-voce.

1. Course Objectives-Program Outcomes (POs) Relationship Matrix (Indicate the relationships by mark "X")

relationships b	y ilia.	IK A	<u>) </u>									
P-Outcomes	Α	В	С	d	е	F	g	h	i	j	k	I
C-Objectives												
1	Χ	Χ	Χ	Χ	Χ				Χ	Χ	Χ	Χ
2	Χ				Χ		Χ	Χ		Χ	Χ	
3	Χ	Χ	Χ			Χ	Χ	Χ	Χ		Χ	Χ
4				Χ	Χ	Х		Χ	Χ	Χ	Χ	
5		Χ	Χ	Χ					Χ	Χ		
6				Χ	Χ	Χ		Χ		Χ	Χ	
7	Χ	Χ	Χ	Χ	Χ	Χ	Χ		X	Χ	X	

2. Course Outcomes-Program Outcomes (POs) Relationship Matrix (Indicate the relationships by mark"X")

P-Outcomes	а	b	С	d	е	f	g	h	i	J	K	I
C-Outcomes												
1	Х	Χ	Х	Х	Х			Х	Х	Х	Х	Х
2	Χ	Χ	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х
3	Χ	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х
4	Χ	Х	Х							Х	Х	Х
5	Χ	Х	Х							Х	Х	Х
6	Χ	Х	Х							Х	Х	Х
7	Χ	Х	Х							Х	Х	Х

3. Courses (with title & code)-Program Outcomes (POs) Relationship Matrix (Indicate the relationships by mark "X"



Department of Electrical & Electronics Engineering

								_				
P-Outcomes	а	b	ပ	d	е	f	g	h		j	K	1
Courses												
Electrical	X	Χ	Х	X	Х	Х	Х	Х	Х	Χ	Х	X
Networks												
Lab												

4. Program Educational Objectives (PEOs) –Vision/Mission Matrix (Indicate therelationships by mark "X")

	Mission of department							
PEOs	Higher Learning	Contemporary Education	Technical knowledge	Research				
Graduates will have a successful Technical or professional careers, Including supportive and leadership roles on multidisciplinary teams	X	X	X	X				
Graduates will be able to acquire, use and develop skills as required for		X	X					
effective professional practices Graduates will be able to attain								
holistic education that is an essential prerequisite for being a responsible member of society	X		X					
Graduates will be engaged in lifelong learning, to remain abreast in their profession and be leaders in our technologically vibrant society.	X		Х	X				

5. Program Educational Objectives(PEOs)-Program Outcomes(POs) Relationship Matrix (Indicate the relationships by m

P- Outcome s	a	b	c	d	e	f
PEOs						
1	X	X	X	X	X	
2	X	X	X	X	X	
3		X	X	X		X
4				X		



Department of Electrical & Electronics Engineering

6.Course Objectives-Course Outcomes Relationship Matrix (Indicate the relationships by mark "X")

	Course-Outcomes	1	2	3	4	5	6	7
	Course-Objectives							
/	1	X	X	X	X	X	X	X
	2	X	X	X	X	X	X	X
	3	X	X					
	4				X	X		
	5			X	X	X	X	X
	6			X	X	X	X	X
	7	X		X	X	X	X	

Program Educational Objectives (PEOs)-Course Outcomes Relationship

Matrix (Indicate the relationships by mark

P-Objectives(PEO)	1	2	3	4
Course-Outcomes				
1	X	X		X
2	X	X		X
3	X	X		X
4	X	X		X
5	X	X		X
6	X	X		X
7	X	X		X

8. Assignments & Assessments-Program Outcomes (POs) Relationship Matrix (Indicate the relationships by mark "X"

P-Outcomes Assessments	A	b	c	d	e	f
1	X	Х		X		X
2	X	X	X			X
3	X	X	X			X
4	X	X	X			X

9. Assignments & Assessments-Program Educational Objectives (PEOs) Relationship Matrix (Indicate the relationships by

P-Objectives (PEOs) Assessments	1	2	3	4
1	X	X		
2		X		
3		X	X	X



Department of Electrical & Electronics Engineering

4	X	
5	X	

Assessment process and Relevant Surveys conducted:

1. Constituencies -Program Outcomes (POs) Relationship Matrix (Indicate the relationships by mark "X").

Constituencies

- 1. Alumni
- 2. Governmentemployers
- 3. Students

P-Qutcomes	a	b	С	d	e	f	G	h	i	j	k	1
Constituencies												
Constituencies												
1	X	X	X	X	X	X	X		X	X		X
2	X	X	X	X	X	X	X		X			X
3	X	X			X	X	X	X		X	X	X

Subject :DC Machines Lab

СО	Cognitive Learning Level						
	1	2	3	4	5	6	
1		Х					
2			Х				
3						Х	
4				Х			
5		Х					
6			Х				
7		Х					

Cognitive Learning Levels:

CLL1: Remembering

CLL2: Understanding

CLL3: Applying

CLL4: Analyzing



Department of Electrical & Electronics Engineering

CLL5: Evaluating

CLL6: Creating

EVALUATION STRATEGY

Academic Year	: 2018-2019		
Semester	: I		
Name of the Program:	EEE B.Tech	II/I	Section: A,B
Course/Subject: DC Machine	es Lab Co	de:GR17A203	37
Name of the Faculty: D.Sriniv	vasa Rao	Dept:	EEE
Designation: Assistant profes	ssor		
1. TARGET:			
A) Percentage for pass: 100%	,		
2. COURSE PLAN & CONTE	NT DELIVERY		
• PPT presentation of t	the Lectures		
Solving exercise prob	blems		
 Model questions 			
3. METHOD OF EVALUATION	ON		
3.1 Daily Attendance			
3.2 Lab records and obse	ervation		
3.3 Mini Projects			
3.4 ☐ Viva Voce			
3.5 Internal Examination	n		
3.6 ☐ Semester/End Exam	nination		
4. List out any new topic(s) or any	y innovation you would like	to introduce in	teaching the subjects in this Semester
Signature of HOD			Signature of faculty
Date:			Date:



Department of Electrical & Electronics Engineering

RUBRIC

OBJECTIVE: Work effectively with others

STUDENT OUTCOME: Ability to function in a multi-disciplinary team

S.No.	Student Name	Performance Criteria	Unsatisfactory	Developing	Satisfactor Y	Exemplar y	Scor e
			1	2	3	4	
1.	R.Madhuri (18245A0218)	Research & Gather Information	Does not collect any information that relates to the topic.	Collects very little information some relates to the topic	Collects some basic Informatio n most relates to the topic.	Collects a great deal of Informati on all relates to the topic.	
		Fulfill team role's	Does not perform any duties of assigned team role.	Performs very little duties.	Performs nearly all duties.	Performs all duties ofassigne d team role.	
	Share Equally	Always relies on others to do the work.	Rarely does the assigned work often needs reminding.	Usually does the assigned work-rarely needs reminding.	Always does the assigned work without having to be reminded		
		Listen to other team mates	Is always talkingnever allows anyone else to speak.	Usually doing most of the talking-rarely	Listens, but sometimes talks too much.	Listens and speaks a fair amount.	



Department of Electrical & Electronics Engineering

		lectrical & El	allows	ngmeenig	
			others to		
			speak.		
					Average score
Revanth 2. (17241A02B0)	Research & Gather Information	Does not collect any information that relates to the topic.	Collects very little informationsome relates to the topic	Collects some basic informatio n-most relates to the topic.	Collects a great deal of informati onall relates to the topic.
	Fulfill team role's	Does not perform any duties of assigned team role.	Performs very little duties.	Performs nearly all duties.	Performs all duties of assigned team role.
	Share Equally	Always relies on others to do the work.	Rarely does the assigned work often needs reminding.	Usually does the assigned work-rarely needs reminding.	Always does the assigned work without having to be reminded .
	Listen to other team mates	Is always talkingnever allows anyone else to speak.	Usually doing most of the talking-rarely allows others to speak.	Listens, but sometimes talks too much.	Listens and speaks a fair amount.



Department of Electrical & Electronics Engineering

						Average score	
3	R.V.Sai Tarun (17241A02A4)	Research & Gather Information	Does not collect any information that relates to the topic.	Collects very little informationsome relates to the topic	Collects some basic informatio nmost relates to the topic.	Collects a great deal of informati onall relates to the topic.	
		Fulfill team role's	Does not perform any duties of assigned team role.	Performs very little duties.	Performs nearly all duties.	Performs all duties of assigned team role.	
		Share Equally	Always relies on others to do the work.	Rarely does the assigned workoften needs reminding.	Usually does the assigned work-rarely needs reminding.	Always does the assigned work without having to be reminded .	
		Listen to other team mates	Is always talkingnever allows anyone else to speak.	Usually doing most of the talking-rarely allows others to speak.	Listens, but sometimes talks too much.	Listens and speaks a fair amount.	
						Average score	





COURSE COMPLETION STATUS

Academic Year	: 2018-2019		
Semester	: I		
Name of the Program:	EEE B.Tecl	hII/I	Section: A,B
Course/Subject: DC Machin	nes Lab	Code:GR17A20	37
Name of the Faculty: D.Srin	Dept:	EEE	

Designation: Assistant professor

Program	Remarks	No. of Objectives Achieved	No. of Outcomes Achieved
1	1 & 2 programs completed by 18/07/18		
2		2,3,	2,4
		4	
3	3 & 4 programs completed by 22/07/18		
4		1,3	2,4
5	5 program completed by 26/07/18	1,3	2,4
6	6 program completed by 29/07/18	1,3	2,4
7	7 program completed by 02/08/18	1,3	2,4
8	8 program completed by 16/08/18	1,3	2,4
9	9 program completed by 23/08/18	1,3	2,4,6
10	10 program completed by 30/08/18	1,3	2,4
11	11 & 12 program completed by 06/09/18	1,2	2,3
12	· · · · · · · · · · · · · · · · · · ·	1,3	2,1,5
13	12 program completed by 13/09/18	1,3	2,1,5,7

Signature of HOD Signature of faculty

Date: Date:

Note: After the completion of each unit mention the number of Objectives & Outcomes Achieved.



Anadamia Vaar



Department of Electrical & Electronics Engineering

GUIDELINES TO STUDY THE COURSE/SUBJECT

Academic Tear	. 2016-2019		
Semester	: I		
Name of the Program:	EEE B.Tecl	nII/I	Section: A,B
Course/Subject: DC Machin	nes Lab	Code:GR17A20	37
Name of the Faculty: D.Srin	ivasa Rao	Dept:	EEE
Designation: Assistant prof	essor		

. 2019 2010

Course Design and Delivery System (CDD):

- The Course syllabus is written into number of learning objectives and outcomes.
- These learning objectives and outcomes will be achieved through lectures, assessments, assignments, experiments in the laboratory, projects, seminars, presentations, etc.
- Every student will be given an assessment plan, criteria for assessment, scheme of evaluation and grading method.
- The Learning Process will be carried out through assessments of Knowledge, Skills and Attitude by various methods and the students will be given guidance to refer to the text books, reference books, journals, etc.

The faculty be able to –

- Understand the principles of Learning
- Understand the psychology of students
- Develop instructional objectives for a given topic
- Prepare course, unit and lesson plans
- Understand different methods of teaching and learning
- Use appropriate teaching and learning aids
- Plan and deliver lectures effectively
- Provide feedback to students using various methods of Assessments and tools of Evaluation
- Act as a guide, advisor, counselor, facilitator, motivator and not just as a teacher alone

Signature of HOD	Signature of faculty
Date:	Date:





ILLUSTRATIVE VERBS FOR STATING INSTRUCTIONAL OBJECTIVES

These verbs can also be used while framing questions for Continuous Assessment Examinations as well as for End – Semester (final)Examinations

ILLUSTRATIVE VERBS FOR STATING GENERAL OBJECTIVES/OUTCOMES							
Know Unders			THE COLUMN	Design			
ILLUSTRATIVE VERBS FOR STATING SPECIFIC OBJECTIVES/OUTCOMES: A. COGNITIVE DOMAIN (KNOWLEDGE)							
1	2	T	3	T	4	5	6
Knowledge	Comprehension Understanding		Application knowledge & omprehension	Analysis Of whole w .r.t. its constituents		Synthesis	Evaluation Judgment
Define Identify			Differentiate Discriminate Distinguish Separate Categorize Combine Design Generate Plan		Combine Design Generate	Compare	
B. <u>AFFECTI</u> (ATTITUI	IVE DOMAIN DE)		C. <u>PSYC</u>	НОМОТО	OR DOMAIN	N (SKILLS)	
Assist	Select		Bend	Dissect	Insert	Perform	Straighten
Change	Develop	ļ	Calibrate	Draw	Keep	Prepare	Strengthen
			Compress F	Extend	Elongate	Remove	Time
			Conduct 1	Feed	Limit	Replace	Transfer
			Connect I	File	Manipulat	te Report	Type
			Convert C	Grow	Move Prec	cisely Reset	Weigh
			Decrease In	ncrease	Paint	Set	





Department of Electrical and Electronics Engineering

B.Tech EEE IIYEAR I SEM RESULT ANALYSIS OF 2017-2021 BATCH

ACADEMIC YEAR 2018-2019 TOTAL. NO. OF STUDENTS REGISTERED = 136

	Total	No. of	No. of			Grad	e Poin	ts			
Subje ct	No. of students appeared	students passed	student s failed	< 5	5	6	7	8	9	10	Pass percentage
ES	136	135	01	00	16	44	51	19	04	01	99.26%
EMF	136	117	19	00	17	22	34	28	19	03	86.02%
NT	136	120	16	00	08	23	30	25	28	06	88.23%
DCM	136	96	40	00	45	26	08	07	05	05	70.58%
DCM Lab	136	135	01	00	09	09	11	18	54	34	99.26%
EN Lab	136	136	00	00	01	10	09	11	44	58	100%
ES Lab	136	136	00	00	04	03	08	22	52	47	100%
SFCV	136	108	28	00	26	31	23	13	12	03	79.41%
СО	136	131	05	00	06	21	40	39	24	01	96.32%

Overall pass (passed in all subjects) = 85/136(62.50%)

Faculty

Environmental Science	Bh Sarojani Rani / Haritha Kirnmayi
Electromagnetic Fields	Syed Sarfaraz Nawaz
Network Theory	M Srikanth
DC Machines	Dr B Phaneendra Babu
Special Functions and Complex Variables	Dr Swapna
Computer Organization	P Ravikanth





DC Machines Lab	P Ravi Kanth/M Prashanth / D Srinivasa Rao
Electrical Networks Lab	Y Satyavani /G Bhaskar Rao
Electrical Simulation Lab	G Sandhya Rani / P Sirisha

ARREARS POSITION – CURRENT YEAR

Descripti	All pass	One	Two	Three	More than Three	% of pass
on		Arrear	Arrears	Arrears	Arrears	
136	85	25	10	04	12	62.50%

Performance overall Class Three Toppers

ROLL NO.	NAME	PERCENTAGE(SGPA)
18245A0222		
	THIRUNAGARU DEEKSHITH	9.46
17241A0203		
	AGGARAPU HARI KRISHNA	9.42
17241A0217		
	DEVASANI PRIYANKA	9.33

HOD,EEE